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<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/643,996	GOTO, AKIO	
	<b>Examiner</b>	<b>Art Unit</b>	
	Jerry T Rahll	2874	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**  
 All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to application received 20 August 2003.
2. ☒ The allowed claim(s) is/are 1-15.
3. ☒ The drawings filed on 20 August 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

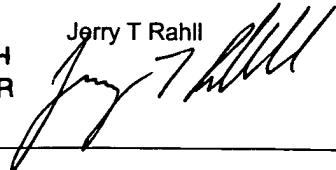
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

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| <ol style="list-style-type: none"> <li>1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)</li> <li>2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br/>Paper No./Mail Date <u>8/20/03</u></li> <li>4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br/>of Biological Material</li> </ol> | <ol style="list-style-type: none"> <li>5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)</li> <li>6. <input type="checkbox"/> Interview Summary (PTO-413),<br/>Paper No./Mail Date _____.</li> <li>7. <input type="checkbox"/> Examiner's Amendment/Comment</li> <li>8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance</li> <li>9. <input type="checkbox"/> Other _____.</li> </ol> |
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**AKM ENAYET ULLAH**  
 PRIMARY EXAMINER

Jerry T Rahll  


**Examiner's Statement of Reasons for Allowance**

***Priority***

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

***Information Disclosure Statement***

2. The information disclosure statement (IDS) submitted on 20 August 2003 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

***Drawings***

3. The drawings submitted on 20 August 2003 have been reviewed and determined to facilitate understanding of the invention. The drawings are accepted as submitted.

***Allowable Subject Matter***

4. The following is an examiner's statement of reasons for allowance. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
5. Claims 1-15 are allowed. Claims 1-15 describe a card type optical transceiver module including an optical receptacle having a photoelectric component for converting electrical signals into optical and optical signals into electrical signals, an electrical connector electrically connected to an electronic device, a motherboard and a childboard which are separately formed and electrically connect the optical receptacle and the electrical connector, a first cutaway portion provided in the motherboard, and a group of pins electrically connect the boards. The

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optical receptacle is supported in a housing forming an outer contour of the module. The first cutaway portion accepts the optical receptacle inside to avoid interference between the optical receptacle and the signals and the motherboard. The childboard is supported by the housing, and the motherboard is supported by the group of pins and incorporated in the housing, and the electrical connector is provided on an edge surface of the housing.

6. U.S. Patent No. 6,402,393 to Grimes et al. describes a card type optical transceiver module including an optical receptacle having a photoelectric component for converting electrical signals into optical and optical signals into electrical signals, an electrical connector electrically connected to an electronic device, a motherboard and a childboard which are separately formed and electrically connect the optical receptacle and the electrical connector and a group of pins electrically connect the boards. The electrical connector is provided on an edge surface of the housing. Grimes et al. does not describe or reasonably suggest a first cutaway portion provided in the motherboard to accept the optical receptacle so as to avoid interference between the optical receptacle and the motherboard.

7. U.S. Patent No. 6,402,393 to Grimes et al. remains the closest prior art of record in this application. For the reasons stated above, however, Claims 1-15 herein are deemed to patentably distinguish over Grimes et al. and all other prior art of record.

### ***Conclusion***

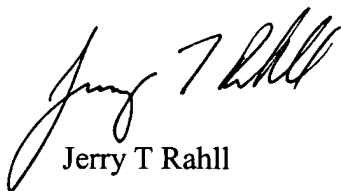
8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. Patents Nos. 5,493,437 and 6,213,651 describe card type optical transceivers.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jerry T Rahll whose telephone number is (571) 272-2356. The examiner can normally be reached on M-F (8:00-5:30), with alternate Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rodney Bovernick can be reached on (571) 272-2344. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Jerry T Rahll



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PRIMARY EXAMINER